

## Design and Development of a PIC Microcontroller Based High Voltage Power Supply

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**Key words:** Microcontroller based oscillator, transistor driver circuit, high voltage transformer, voltage multiplier circuit, PIC microcontroller and MPLAB IDE, complete, HV

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**Abstract:** Design, development and simulation of a microcontroller based High Voltage Power Supply (HVPS) have been presented in this study. The HVPS consists of microcontroller ( $\mu C$ ) based oscillator, transistor driver circuit, ferrite core High Voltage (HV) transformer and quadruple voltage multiplier circuit. The PIC16F84A  $\mu C$  with 20 MHz crystal generates 9.09 kHz, 96.36%D, 5 V signal which has been used as a input for transistor 2N2222A driver coupled with ferrite core HV transformer. Thereafter, the transformer's secondary voltage has been multiplied by a quadrupler circuit. A multi-turn potentiometer would be used to control the output voltage to a required level. An assembly language program based on MPLAB IDE has been developed for generation of the oscillator signal. The complete High Voltage Power Supply (HVPS) has been designed and verified in Proteus 7.7 simulation platform.

## INTRODUCTION

A power supply irrespective of category Low Voltage Power Supply (LVPS) or High Voltage Power Supply (HVPS) must provide stable and ripple-free DC output voltage independent of line and load variations. There are mainly two types of High Voltage Power Supply (HVPS) available in the nuclear field: those for very low current upto 100  $\mu A$  used for biasing semiconductor detectors and gas-filled detectors. And those able to deliver higher currents needed when working with photo multiplier tubes (IAEA., 1986). In this regard, variable high voltage power supply with over-current alarm and time delay that gives 0-300 V and 100 mA based on a simple circuit employing zener diodes and series pass transistor for its regulation (Islam and Quamruzzaman, 1992). By Islam and Quamruzzaman (1992), a variable high voltage power supply using phase angle control has been presented.

The high voltage output is adjustable from 100-1000 V and the current rating is 10 mA. Design, fabrication and performance study of a low cost high voltage power Supply provides 100-900 V and 12.5 mA constructed with astable circuit, monostable circuit, transistor driver circuit, HV X-former and voltage multiplier circuit (Islam *et al.*, 2012).

In the current research, a variable high voltage power supply with the introduction of a microcontroller based oscillator (9.09 kHz, 96.36%D, 5 V) replacing as



Fig. 1: Block diagram of the proposed microcontroller based high voltage power supply

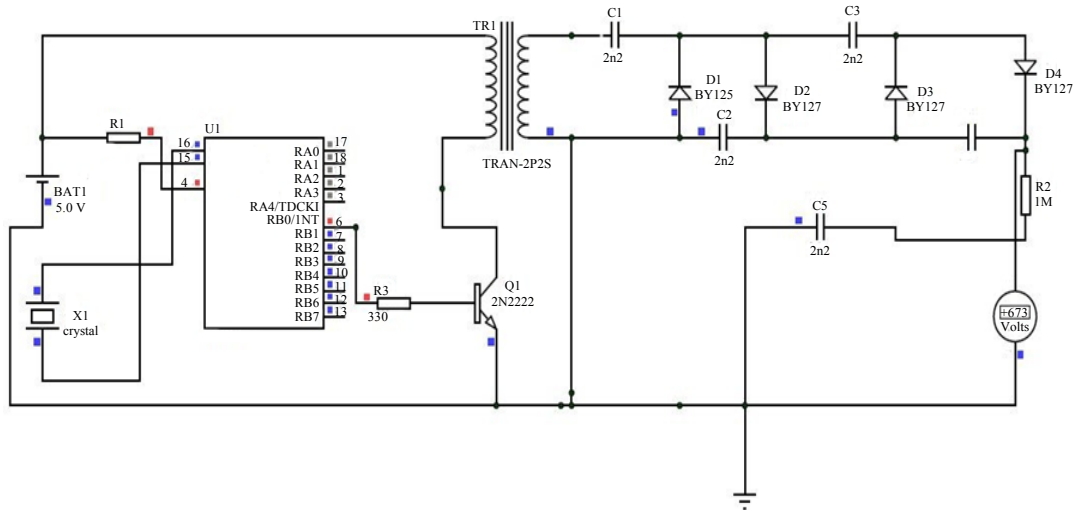


Fig. 2: Schematic diagram of the proposed microcontroller based high voltage power supply

table circuit and monostable circuit in previous research (Islam *et al.*, 2012) has been presented. The output voltage is ranging from 0-673 V. The schematic of this HVPS has been designed using Proteus 7.7 for electronics design and performance verification. The following blocks shown in Fig. 1 comprise the designed High Voltage Power Supply (HVPS).

## MATERIALS AND METHODS

A microcontroller based High Voltage Power Supply (HVPS) consists of the microcontroller PIC16F84A with 20 MHz crystal, driver transistor 2N2222A, ferrite core HV transformer, voltage multiplier circuit and Low Voltage Power Supply (LVPS) have been presented. Block diagram, schematic diagram, circuit description, functional description and program flowchart for the designed HVPS also have been presented accordingly.

**Block diagram:** Block diagram of the proposed HVPS comprises of the PIC microcontroller based oscillator, driver circuit, ferrite core HV transformer, voltage multiplier circuit as shown in Fig. 1.

**Schematic diagram:** Complete simulation model of the  $\mu$ C based High Voltage Power Supply (HVPS) has been shown in Fig. 2. The description of individual circuit has been given.

**Low voltage power supply:** The Low Voltage Power Supply (LVPS) is essential for the system and a built-in 5 V DC power supply has been used in Proteus 7.7 simulation platform.

**Microcontroller based oscillator:** The oscillator is an electronic circuit that produces a periodic, oscillating

electronic signal, often a sine wave or a square wave (Anonymous, 2019a-c). In this context, the microcontroller based oscillator PIC16F84A  $\mu$ C with 20 MHz crystal X1 generates 9.09 kHz, 96.36%D, 5 V signal. The PIC16F84A is a 18-pin DIP package enhanced flash microcontroller. It consists of CPU, memory for data and program, Special Function Registers (SFRs), General Purpose Registers (GPRs), 2-port (PortA&PortB), active low MCLR, interrupts and free-run timer TMR0 and so on (Anonymous, 2001; Islam *et al.*, 2013). An assembly language program based on MPLAB IDE has been developed for generation of the oscillator signal. The microcontroller reset pin MCLR has been connected to Vcc through resistor R1.

**Transistor driver circuit:** Low power bipolar transistor Q1 2N2222 features are NPN silicon planar switching transistors, switching and linear application DC and VHF amplifier applications (Anonymous, 2019a-c) has been used in this HVPS to drive the transformer TR1 TRAN-2P2S with a view to circuit test and simulation purposes only instead of a ferrite core high voltage transformer as collector load and resistor R3 as base bias.

**High voltage transformer:** Transformers are used for increasing or decreasing the alternating voltages in electric power applications and for coupling the stages of signal processing circuits. Ferrite cores are used for high voltage transformer at higher frequencies, typically between a few tens of kilohertz to hundreds of megahertz to reduce losses, physical size and weight of inductive components. The primary voltage at the step-up ferrite-core transformer is adjusted according to the required output voltage.

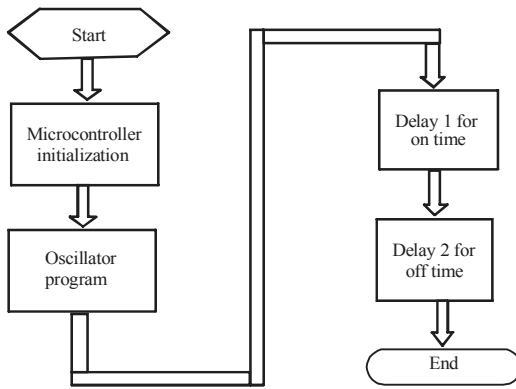


Fig. 3: Shows the program flowchart for the developed HVPS

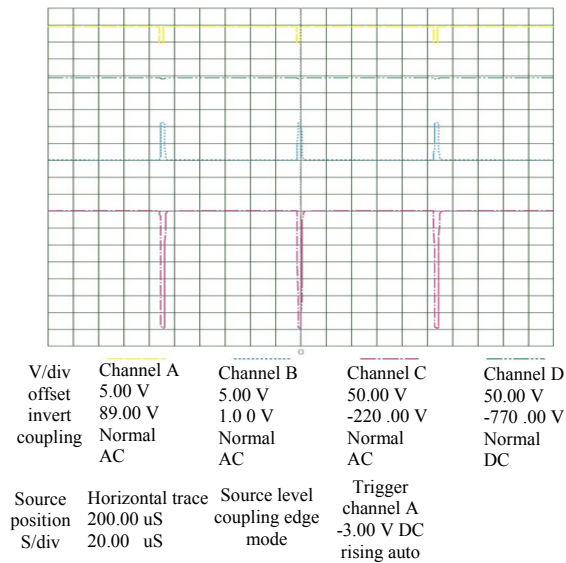


Fig. 4: Shows the input and output waveforms for the designed HVPS



Fig. 5: Shows the basic building blocks of a High Voltage Power Supply (HVPS)

**Voltage multiplier circuit:** A voltage multiplier can be understood as a series of half-wave rectifiers connected in cascade. It has been observed that D1 charges C1 to the peak, Voltage  $V_p$ . This wave is rectified through D2 and C2 is then charged to  $2V_p$ . The same analysis determines that the peak voltage  $2V_p$  which rectified through D3, charges C3 to  $3V_p$ . Likewise, this wave is rectified through D4 and C4 is then charged to  $4V_p$ .

**Output filter circuit:** The output filter circuit for the designed HVPS comprises of Resistor R2 and Capacitor C5.

## RESULTS AND DISCUSSION

Design, development and simulation of a microcontroller based High Voltage Power Supply (HVPS) have been presented in this research. The block diagram, schematic diagram, circuit description, functional description and program flowchart for the designed HVPS also have been presented accordingly.

The microcontroller based oscillator PIC16F84A  $\mu C$  with 20 MHz crystal X1 generates 9.09 kHz, 96.36%D, 5 V signal as shown in Fig. 2 and 4 shows the program flowchart for the designed HVPS where an assembly language program based on MPLAB IDE has been developed for generation of the oscillator signal. The microcontroller reset pin MCLR has been connected to Vcc through Resistor R1. Low power bipolar transistor Q1 2N2222 features are NPN silicon planar switching transistors has been used in this HVPS to drive TR1 TRAN-2P2S with a view to circuit test and simulation purposes only instead of a ferrite core high voltage transformer as collector load and Resistor R3 as base bias. The primary voltage at the step-up ferrite-core transformer is adjusted according to the required output voltage. Ferrite cores are used in the high voltage transformer at higher frequencies, typically between a few tens of kilohertz to hundreds of megahertz to reduce losses, physical size and weight of inductive components. The output of this circuit thereafter multiplied by using voltage multiplier circuit. The output filter circuit for the designed HVPS comprises of Resistor R2 and Capacitor C5. The input and output waveforms of the developed system has been shown in Fig. 3. Four channel digital oscilloscope consisting of channel A shows the input signal, channel B for transformer's primary voltage. The channel C and channel D show transformer's secondary voltage and HVPS output voltage in the Proteus 7.7 simulation platform, respectively.

In the current research, a variable high voltage power supply with the introduction of a microcontroller based oscillator (9.09 kHz, 96.36%D, 5 V) replacing as table circuit and monostable circuit in previous research (Islam *et al.*, 2012) has been presented. The output voltage is ranging from 0-673 V as shown in Fig. 4. The schematic of this HVPS has been designed using Proteus 7.7 for electronics design and performance verification. The basic building blocks of a High Voltage Power Supply (HVPS) consist of an oscillator, step-up transformer and rectifier circuit as shown in Fig. 5. Likewise, the designed High Voltage Power Supply

(HVPS) comprises the following blocks as shown in Fig. 1 such as microcontroller based oscillator transistor driver circuit, HV X-former and voltage multiplier circuit.

The High Voltage Power Supply (HVPS) is very important electronic instrument in the conventional Nuclear Instrumentation Module (NIM) standard or stand-alone type of the nuclear field for biasing various nuclear detectors.

### CONCLUSION

In this research, design, development and simulation of a microcontroller based High Voltage Power Supply (HVPS) consists of the microcontroller PIC16F84A with 20 MHz crystal, driver transistor 2N2222A, ferrite core HV transformer, voltage multiplier circuit and Low Voltage Power Supply (LVPS) have been presented. The design and verification of the PIC  $\mu$ C based HVPS in Proteus 7.7 simulation platform has been completed successfully. The designed system is cost effective, simple and reliable in operation. The HVPS has been tested repeatedly and its performance was found satisfactory.

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